

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No.10/760,087
Filing Date January 14, 2004
Confirmation No. 2443
Inventor..... Arup Bhattacharyya
AssigneeMicron Technology, Inc.
Group Art Unit 3663
ExaminerJohannes P. Mondt
Attorney's Docket No. MI22-2473
Title:.....Computer Systems Utilizing High Performance Three-dimensional
TFT-Based CMOS Inverters

LETTER SUBMITTING FORMAL DRAWINGS

To: Mail Stop
 Commissioner for Patents
 P.O. Box 1450
 Alexandria, VA 22313-1450

From: Paul S. Holdaway (Tel. 509-624-4276; Fax 509-838-3424)
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REMARKS

Please enter the enclosed formal drawings in the above-referenced application in place of the originally-filed drawings. The proposed changes are indicated in red on the attached annotated copies of sheets 4, 6, and 7. The formal drawings marked as "Replacement Sheets" incorporate the proposed amendments and number sheets 4, 6, and 7 represented amended Figs. 9, 11, and 12.

Acknowledgement receipt of the formal drawing sheets and their acceptance into the file is requested.

Respectfully submitted,

Dated: 24 June 2008

By: Paul S. Holdaway
Paul S. Holdaway
Reg. No. 56,376

Enclosures: Replacement Sheets 4, 6, and 7 (Figs. 1-18) and
Annotated sheets 4, 6, and 7 (Figs 8-9, 11, and 12)